FEDEVEL ACADEMY

CERTIFICATION

This is to certify that

George Zarnescu

has successfully completed a course

Schematic & PCB Design

Date: **02 NOV 2015 - 10 JAN 2016**

Course tutor *Robert Feranec*

Software used: **Altium Designer**
Course content: 1) Starting a new project 2) Schematic design 3) Selecting components and creating libraries 4) Footprints, 3D models, starting a new PCB 5) Variants & BOMs 6) Checking libraries and schematic 7) PCB layout & Placement 8) PCB layout & Impedances, stackup 9) Stackup, finishing PCB, length matching 10) Generating output documentation

Certificate number: 32d3f51cd2e94516a69c46bde4cf7ce2

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